E E 2 7 5

Digital Signal Processing

Spring Semester 2015

COURSE ORGANIZATION

HOMEWORK
Homeworks assigned weekly.
They will be collected and graded.
Late homeworks will not be accepted for grading.

EXAMS
There will be two in-class exams and one Final exam.

GRADING
Homework (25%)
Exams I & II (20% each)
Final (35 %)

EE289, DSP LAB
EE289 (1-credit hr) is the Lab. part of this course. It is the learning by doing part.
There will be 3 Lab. sections. There will be a one-half hour Lab Lectures before each Lab.
Labs will be held in the Signal and Image Processing Lab in 330 Votey.

INSTRUCTOR
G. Mirchandani 355 Votey Office Hours: 10:00 - 11:00 a.m. Tues, Thus.
mirchand@cems.uvm.edu 656-4587

LAB INSTRUCTOR
Ramon Zhi 330 Votey Office Hours: tba
Ruoyu.Zhi@uvm.edu